Imitation of Life:

# Advanced system for native Artificial Evolution

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## Abstract:

A model for artificial evolution in native x86 Windows systems has been developed at the end of 2010. In this text, further improvements and additional analogies to natural microbiologic processes are presented. Several experiments indicate the capability of the system - and raise the question of possible countermeasures.

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## 1 Introduction

Artificial Evolution has become a successful playground for evolutional experiments, when Tom Ray released the Tierra system.[1]. Tierra is a virtual system with self-replicating programs which simulate mutations in form of copying errors. The artificial creatures struggle for the limited resources (such as CPU time and memory space), thus the systems fulfills the three criteria for evolution: replication, mutation, selection.

In order to achieve high robustness against mutations, Ray introduced techniques such as non-direct addressing and separation of arguments and operations.

Many interesting insights to evolution have been found with Tierra (such as evolution of multi-cellularity[2] or parallel computing[3]) and similar systems such as avida (evolution under high mutation rate[4] and emergence of complex features[5]).

Iliopoulos, Adami and Ször have discussed the consequences for computer security of implementing darwinian principles into native system, in 2008.[7] They concluded *that a truly undetectable virus might be more feasible than previously imagined*, and that it is currently unknown whether there would be a defence against such organisms.

In 2010, I have created the first (to my knowledge) implementation of an artificial evolution system for a native operation system (Microsoft Windows XP+ 32bit), using several parallels to the natural biosynthesis process[8]. A short comparison between usual x86 code and the new artificial evolution concept shows that the new concept is actually more robust against mutations.[9]

The presented proof-of-concept organism as well as the underlaying metalanguage have been analysed in detail by Peter Ferrie in 2011[10][11].

The idea of this article is to continue this research...

# 2 Artificial Biosynthesis

The main idea is to use a similar concept to natural biosynthesis: The codons of the mRNA are translated into amino acids using tRNA molecules, these amino acid chains form the proteins - the actually functional part of the cell.

In the artificial analogon, the whole information of the organism is saved in a chain of codons. Each codon consists of 8bits, thus there are 256 different codons. In the translator (similar to tRNA in the ribosom), the codons will be mapped to an x86 instruction (similar to an amino acid) - a chain of x86 instructions form the protein, the functional part of the organism:

Artificial	Natural
bit	nucleobase
byte	$\operatorname{codon}$
instruction	amino acid
function	protein
$\operatorname{translator}$	$\mathrm{tRNA}$

In the natural system, the information is saved in codons, which consists of 3 nucleobases - as there exists 4 different nucleobases (Adenine, Guanine, Thymine, Cytosine), a codon can have  $4^3 = 64$  different representations. Each codon codes one out of 20 amino acids, thus there is a redundancy in the mapping process, which is used to increase the robustness of the code. This redundancy is also used in the artificial biosynthesis as there are less than  $2^8=256$  base functions of the meta language.

#### 2.1 Meta-Language

The idea is to create a compact, *complete* instruction set with seperation of arguments and operations, and with non-direct addressing.

The language provides seven registers with specific properties:

- RegA, RegB, RegD general purpose registers (correspond to EAX, EBX, EDX)
- **BC1** operation register (correspond to EBX): the first argument of every operation, and source or destination for other instructions
- BC2 argument register (correspond to ECX): the second argument of every operation
- **BA1** write address register (correspond to EDI): holds the address for write instructions
- **BA2** jump address register (correspond to ESI): holds the address for jump instructions

The separation of arguments and operations is realized by using BC1 (and BC2) as standard arguments, and filling them independently of the operation.

The language provides PIC (*position-independent code*). Every address is relative to the instruction pointer thus is independent of the position.

nopREALnopnopsABC1 = RegAmov ebx, eaxnopsBBC1 = RegBmov ebx, ebpnopsDBC1 = RegDmov ebx, edxnopdARegA = BC1mov eax, ebxnopdBRegB = BC1mov edx, ebxsaveBC2 = BC1mov ecx, ebxaddsavedBC1 + = BC2add ebx, ecxsubsavedBC1 - = BC2sub edx, ecxsave.BC1 = BC2sub edx, ecxsave.BC1 = BC1mov edi, ebxsave.BC1 = BC2sub edx, ecxsave.BC1 = BC1mov edi, ebxsave.BC1 = BC1mov edidel, ebxsave.BC1 = Ibst edifiestmov ebx, DataOffsetgetDOBC1 = DataOffsetmov ebx, dvard[ebx]gettataBC1 = InstructionPointercall gEIP; gEIP; pop ebxpushpush BC1push ebxpop1popA1popA4popA1gopA4popA4add0001BC1 + = 0x10add ebx, 0x10add0001BC1 + = 0x40add ebx, 0x40add0004BC1 + = 0x400add ebx, 0x400add0000BC1 + = 0x400 <th>Instruction</th> <th>HLL</th> <th>Assembler</th>	Instruction	HLL	Assembler
nopsBBC1 = RegBnov elx, ebpnopsDBC1 = RegDmov elx, edxnopdARegA = BC1mov eax, ebxnopdBRegB = BC1mov eax, ebxnopdDRegD = BC1mov edx, ebxsaveBC2 = BC1mov edx, ebxaddsavedBC1 + = BC2add ebx, ecxsubsavedBC1 - = BC2sub ebx, ecxsaveWrtOffBA1 = BC1mov edi, ebxsaveWrtOffBA2 = BC1mov edi, ebxsaveWrtOffBA1 = BC1mov edi, ebxsaveWrtOffBA1 = BC1mov edi, ebxsaveWrtOffBA1 = BC1mov edi, ebxsaveUrtOffBC1 = DataOffsetmov exix, DataOffsetgetDOBC1 = DataOffsetmov ebx, DataOffsetgetDDBC1 = InstructionPointercall gEIP; gEIP; pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushalpushadpopadpopadpopadadd0001BC1 + = 0x1add ebx, 0x1add0004BC1 + = 0x40add ebx, 0x40add0004BC1 + = 0x40add ebx, 0x40add0000BC1 + = 0x400add ebx, 0x400add0000BC1 + = 0x100add ebx,	nopREAL		nop
nopsDBC1 = RegDmov ebx, edxnopdARegA = BC1mov eax, ebxnopdBRegB = BC1mov eax, ebxnopdDRegD = BC1mov eax, ebxaddsavedBC2 = BC1mov ecx, ebxaddsavedBC1 = BC2sub ebx, ecxsaveBC2 = BC1mov edi, ebxsavedBC1 = BC2sub ebx, ecxsaveMrtOffBA1 = BC1mov edi, ebxsaveJmpOffBA2 = BC1mov edi, ebxwriteBytebyte[BA1] = (BC1 & 0xFF)mov bte[edi], blwriteDWorddword[BA1] = BC1mov ebx, DataOffsetgetDOBC1 = DataOffsetmov ebx, DataOffsetgetBIPBC1 = InstructionPointercall gEIP; gEIP: pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushallpushadpushadpopadlpopadpopadadd0001BC1 + = 0x1add ebx, 0x1add0004BC1 + = 0x1add ebx, 0x10add0005BC1 + = 0x10add ebx, 0x10add0006BC1 + = 0x10add ebx, 0x40add0007BC1 + = 0x10add ebx, 0x400add0000BC1 + = 0x100add ebx, 0x400add0000BC1 + = 0x4000add ebx, 0x400add0000<	nopsA	BC1 = RegA	mov ebx, eax
nopdARegA = BC1mov eax, ebxnopdBRegB = BC1mov eax, ebxnopdDRegD = BC1mov edx, ebxsaveBC2 = BC1mov ecx, ebxaddsavedBC1 + = BC2add ebx, ecxsubsavedBC1 - = BC2sub ebx, ecxsaveJmpOffBA1 = BC1mov edi, ebxsaveJmpOffBA2 = BC1mov edi, ebxwriteBytebyte[BA1] = (BC1 & 0xFF)mov byte[edi], blwriteDWorddword[BA1] = BC1mov ebx, DataOffsetgetDOBC1 = DataOffsetmov ebx, dword[ebx]getBPBC1 = InstructionPointercall gEIP; gEIP: pop ebxpushpush BC1pop ebxpoppop BC1pop ebxpushallpushadpushadpopallpopadzdd ebx, 0x1add0001BC1 + = 0x1add ebx, 0x4add0001BC1 + = 0x10add ebx, 0x40add0010BC1 + = 0x10add ebx, 0x40add0010BC1 + = 0x10add ebx, 0x400add0010BC1 + = 0x100add ebx, 0x400add0010BC1 + = 0x100add ebx, 0x400add0000BC1 + = 0x100add ebx, 0x400 <th>nopsB</th> <th>BC1 = RegB</th> <th>mov ebx, ebp</th>	nopsB	BC1 = RegB	mov ebx, ebp
nopdBRegB = BC1mov ebp, ebxnopdDRegD = BC1mov edx, ebxsaveBC2 = BC1mov ecx, ebxaddsavedBC1 + = BC2add ebx, ecxsubsavedBC1 - = BC2sub ebx, ecxsaveJmpOffBA1 = BC1mov edi, ebxsaveJmpOffBA2 = BC1mov esi, ebxwriteDWorddword[BA1] = BC1mov edid[edi], ebxgetDOBC1 = DataOffsetmov ebx, DataOffsetgetDDBC1 = InstructionPointercall gEIP; gEIP: pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushallpushadpushadpopallBC1 + 0x1add ebx, 0x1add0001BC1 + 0x1add ebx, 0x1add0001BC1 + 0x1add ebx, 0x1add0001BC1 + 0x1add ebx, 0x4add0001BC1 + 0x1add ebx, 0x40add0000BC1 + 0x1add ebx, 0x40add0001BC1 + 0x10add ebx, 0x40add0000BC1 + 0x10add ebx, 0x40add0000BC1 + 0x40add ebx, 0x40add0000BC1 + 0x400add ebx, 0x400add0000BC1 + 0x400ad	nopsD	BC1 = RegD	mov ebx, edx
nopdDRegD = BC1mov eds, ebxsaveBC2 = BC1mov ex, ebxaddsavedBC1 + = BC2add ebx, ecxsubsavedBC1 - = BC2sub ebx, ecxsaveWrtOffBA1 = BC1mov edi, ebxsaveJmpOffBA2 = BC1mov esi, ebxwriteBytebyte[BA1] = (BC1 & 0xFF)mov tel(di), b1writeDWorddword[BA1] = BC1mov ebx, dword[edi], ebxgetDOBC1 = DataOffsetmov ebx, dword[ebx]gettBPBC1 = InstructionPointercall gEIP; gEIP: pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushallpushadpopadzerOBC1 = 0mov ex, 0x0add0001BC1 + = 0x1add ebx, 0x1add0001BC1 + = 0x10add ebx, 0x4add0004BC1 + = 0x10add ebx, 0x400add0000BC1 + = 0x400add ebx, 0x400add0000BC1 + = 0x100add ebx, 0x400add1000BC1 + = 0x100add ebx, 0x400add0001BC1 + = 0x100add ebx, 0x400add0000BC1 + = 0x100add ebx, 0x400add0000BC1 + = 0x100a	nopdA	RegA = BC1	mov eax, ebx
save $BC2 = BC1$ mov ecx, ebxaddsaved $BC1 + = BC2$ add ebx, ecxsubsaved $BC1 - = BC2$ sub ebx, ecxsaveWrtOff $BA1 = BC1$ mov edi, ebxsaveJmpOff $BA2 = BC1$ mov edi, ebxwriteBytebyte[BA1] = (BC1 & 0xFF)mov byte[edi], blwriteDWorddword[BA1] = BC1mov dword[edi], ebxgetDO $BC1 = DataOffset$ mov ebx, DataOffsetgetDD $BC1 = DataOffset$ mov ebx, dword[ebx]getEIP $BC1$ mov ebx, dword[ebx]poppop BC1pop ebxpushallpushadpushadpopallpopadpopadzer0 $BC1 = 0$ mov ebx, 0x0add0001 $BC1 + 0x1$ add ebx, 0x1add0004 $BC1 + 0x1$ add ebx, 0x1add0010 $BC1 + 0x1$ add ebx, 0x10add0010 $BC1 + 0x10$ add ebx, 0x10add0010 $BC1 + 0x10$ add ebx, 0x10add0010 $BC1 + 0x10$ add ebx, 0x100add0010 $BC1 + 0x100$ add ebx, 0x100add0010 $BC1 + 0x100$ add ebx, 0x100add0000 $BC1 + 0x100$ add ebx,	nopdB	RegB = BC1	mov ebp, ebx
addsaved $BC1 + = BC2$ add ebx, ecxsubsaved $BC1 - = BC2$ sub ebx, ecxsaveWrtOff $BA1 = BC1$ mov edi, ebxsaveJmpOff $BA2 = BC1$ mov edi, ebxwriteBytebyte[BA1] = (BC1 & 0xFF)mov byte[edi], blwriteDWorddword[BA1] = BC1mov ebx, DataOffsetgetDO $BC1 = DataOffset$ mov ebx, dword[ebx]getLP $BC1 = InstructionPointer$ call gEIP; gEIP: pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushallpushadpushadpopallpopadpopadzerO $BC1 = 0$ mov ebx, 0x0add0001 $BC1 + = 0x1$ add ebx, 0x1add0001 $BC1 + = 0x1$ add ebx, 0x4add0010 $BC1 + = 0x10$ add ebx, 0x40add0010 $BC1 + = 0x10$ add ebx, 0x40add0010 $BC1 + = 0x10$ add ebx, 0x40add0000 $BC1 + = 0x10$ add ebx, 0x40add0000 $BC1 + = 0x10$ add ebx, 0x40add0010 $BC1 + = 0x100$ add ebx, 0x400add0000 $BC1 + = 0x400$ add ebx, 0x400add000	nopdD	RegD = BC1	mov edx, ebx
subsaved $BC1 - = BC2$ sub ebx, ecxsaveWrtOff $BA1 = BC1$ mov edi, ebxsaveJmpOff $BA2 = BC1$ mov edi, ebxwriteBytebyte[BA1] = (BC1 & 0xFF)mov byte[edi], blwriteDWorddword[BA1] = BC1mov ebx, DataOffsetgetDO $BC1 = DataOffset$ mov ebx, dword[ebx]getEIP $BC1 = InstructionPointer$ call gEIP; gEIP: pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushallpushadpushadpopallpopadpopadadd0001 $BC1 + = 0x1$ add ebx, 0x1add0004 $BC1 + = 0x4$ add ebx, 0x40add0010 $BC1 + = 0x40$ add ebx, 0x40add0010 $BC1 + = 0x40$ add ebx, 0x40add0100 $BC1 + = 0x400$ add ebx, 0x400add0100 $BC1 + = 0x400$ add ebx, 0x400add0100 $BC1 + = 0x400$ add ebx, 0x400add0100 $BC1 + = 0x400$ add ebx, 0x400add0000 $BC1 + = 0x400$ add ebx, 0x400add0100 $BC1 + = 0x400$ add ebx, 0x400add0100 $BC1 + = 0x400$ add ebx, 0x400add0100 $BC1 + = 0x400$ add ebx, 0x400add0000 $BC1 + = 0x400$ add ebx, 0x400 </th <th>save</th> <th>BC2 = BC1</th> <th>mov ecx, ebx</th>	save	BC2 = BC1	mov ecx, ebx
saveWrtOff $BA1 = BC1$ mov edi, ebxsaveJmpOff $BA2 = BC1$ mov esi, ebxwriteBytebyte[BA1] = (BC1 & 0xFF)mov byte[edi], blwriteDWorddword[BA1] = BC1mov dword[edi], ebxgetDOBC1 = DataOffsetmov ebx, DataOffsetgetataBC1 = InstructionPointercall gEIP; gEIP: pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushallpushadpushadpopallBC1 + = 0x1add ebx, 0x1add0001BC1 + = 0x4add ebx, 0x4add0004BC1 + = 0x40add ebx, 0x40add0010BC1 + = 0x400add ebx, 0x400add0004BC1 + = 0x400add ebx, 0x400add0000BC1 + = 0x100add ebx, 0x400add1000BC1 + = 0x100add ebx, 0x4000add1	addsaved	BC1 + = BC2	add ebx, ecx
saveJmpOff $BA2 = BC1$ mov esi, ebxwriteBytebyte[BA1] = (BC1 & 0xFF)mov byte[edi], blwriteDWorddword[BA1] = BC1mov dword[edi], ebxgetDO $BC1 = DataOffset$ mov ebx, $DataOffset$ getdata $BC1 = dword[BC1]$ mov ebx, $dword[ebx]$ getEIP $BC1 = InstructionPointer$ call gEIP; gEIP: pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushallpushadpushadpopallpopadgetadadd0001 $BC1 + 0x1$ add ebx, $0x1$ add0004 $BC1 + 0x1$ add ebx, $0x1$ add0010 $BC1 + 0x40$ add ebx, $0x40$ add0010 $BC1 + 0x40$ add ebx, $0x40$ add0004 $BC1 + 0x40$ add ebx, $0x400$ add0000 $BC1 + 0x400$ add ebx, $0x400$ add0000 $BC1 + 0x100$ add ebx, $0x400$ add0000 $BC1 + 0x400$ add ebx, $0x400$ add0000 $BC1 + 0x400$ add ebx, $0x400$ add0000 $BC1 + 0x100$ add ebx, $0x400$ add0000 $BC1 + 0x100$ add ebx, $0x4000$ add0000 $BC1 + 0x100$ add ebx, $0x4000$ add0000 $BC1 + 0x1000$ add ebx, $0x4000$ add0000 $BC1 + 0x1000$ add ebx, $0x1000$ add0000 $BC1 + 0x1000$ add ebx, $0x1000$ add0000 $BC1 + 0x1000$ add ebx, $0x1000$ add1000 $BC1 + 0x1000$ add ebx, $0x1000$ add20001 $BC1 - 0x1$ sub ebx, $0x1$ shl $BC1 < BC2$ <th< th=""><th>subsaved</th><th>BC1 - BC2</th><th>sub ebx, ecx</th></th<>	subsaved	BC1 - BC2	sub ebx, ecx
writeBytebyte[BA1] = (BC1 & 0xFF)mov byte[edi], blwriteDWorddword[BA1] = BC1mov dword[edi], ebxgetDOBC1 = DataOffsetmov ebx, DataOffsetgetdataBC1 = dword[BC1]mov ebx, dword[ebx]getEIPBC1 = InstructionPointercall gEIP; gEIP: pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushallpushadpushadpopallBC1 = 0mov ebx, 0x0add0001BC1 + = 0x1add ebx, 0x1add0004BC1 + = 0x4add ebx, 0x1add0010BC1 + = 0x10add ebx, 0x40add0010BC1 + = 0x10add ebx, 0x40add0010BC1 + = 0x10add ebx, 0x400add0000BC1 + = 0x100add ebx, 0x400add0100BC1 + = 0x100add ebx, 0x400add0100BC1 + = 0x100add ebx, 0x400add0400BC1 + = 0x100add ebx, 0x400add0400BC1 + = 0x100add ebx, 0x400add0000BC1 + = 0x100add ebx, 0x1000add0400BC1 + = 0x100add ebx, 0x1000add0400BC1 + = 0x100add ebx, 0x1000add0400BC1 + = 0x100add ebx, 0x1000add0001BC1 + = 0x10add ebx, 0x1add0001BC1 + = 0x100add ebx, 0x1000add0000BC1 + = 0x100add ebx, 0x1000	saveWrtOff	BA1 = BC1	mov edi, ebx
writeDWorddword[BA1] = BC1mov dword[edi], ebxgetDOBC1 = DataOffsetmov ebx, DataOffsetgetdataBC1 = dword[BC1]mov ebx, dword[ebx]getEIPBC1 = InstructionPointercall gEIP; gEIP: pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushallpushadpushadpopallpopadgetEXadd0001BC1 + = 0x1add ebx, 0x1add0004BC1 + = 0x4add ebx, 0x4add0010BC1 + = 0x40add ebx, 0x40add0010BC1 + = 0x40add ebx, 0x400add0100BC1 + = 0x40add ebx, 0x400add0100BC1 + = 0x400add ebx, 0x400add0100BC1 + = 0x100add ebx, 0x400add0100BC1 + = 0x400add ebx, 0x400add0100BC1 + = 0x400add ebx, 0x400add0400BC1 + = 0x400add ebx, 0x400add0400BC1 + = 0x400add ebx, 0x400add0100BC1 + = 0x100add ebx, 0x400add0100BC1 + = 0x100add ebx, 0x400add0100BC1 + = 0x100add ebx, 0x4000add0100BC1 + = 0x	saveJmpOff	BA2 = BC1	mov esi, ebx
get DOBC1 = DataOffsetmov ebx, DataOffsetgetdataBC1 = InstructionPointercall gEIP; gEIP: pop ebxget EIPBC1 = InstructionPointercall gEIP; gEIP: pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushallpushadpushadpopallpopadpopadadd0001BC1 + = 0x1add ebx, 0x1add0004BC1 + = 0x4add ebx, 0x4add0010BC1 + = 0x4add ebx, 0x40add0010BC1 + = 0x40add ebx, 0x40add0010BC1 + = 0x40add ebx, 0x40add0010BC1 + = 0x40add ebx, 0x400add0100BC1 + = 0x100add ebx, 0x400add0100BC1 + = 0x400add ebx, 0x400add1000BC1 + = 0x400add ebx, 0x400add40400BC1 + = 0x100add ebx, 0x400add4000BC1 + = 0x100add ebx, 0x4000add4000BC1 + = 0x100add ebx, 0x4000addebx, 0x1BC1 - = 0x1sub ebx, 0x1shlBC1 << (BC2 & 0xFF)	writeByte	byte[BA1] = (BC1 & 0xFF)	mov byte[edi], bl
getdataBC1 = dword[BC1]mov ebx, dword[ebx]getEIPBC1 = InstructionPointercall gEIP; gEIP: pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushallpushadpushadpopallpopadgopadzer0BC1 = 0mov ebx, 0x0add0001BC1 + = 0x1add ebx, 0x1add0004BC1 + = 0x4add ebx, 0x4add0010BC1 + = 0x10add ebx, 0x40add0100BC1 + = 0x10add ebx, 0x40add0100BC1 + = 0x100add ebx, 0x400add0100BC1 + = 0x100add ebx, 0x400add0400BC1 + = 0x100add ebx, 0x400add0010BC1 + = 0x100add ebx, 0x400add0100BC1 + = 0x100add ebx, 0x400add0010BC1 + = 0x100add ebx, 0x400add0100BC1 + = 0x100add ebx, 0x400add0100BC1 + = 0x2xoradd ebx, 0x1shlBC1 < < (BC2 & 0xFF)	writeDWord	dword[BA1] = BC1	mov dword[edi], ebx
getEIP $BC1 = InstructionPointer$ call gEIP; gEIP: pop ebxpushpush BC1push ebxpoppop BC1pop ebxpushallpushadpushadpopallpopadpopadzer0 $BC1 = 0$ mov ebx, 0x0add0001 $BC1 + = 0x1$ add ebx, 0x1add0004 $BC1 + = 0x1$ add ebx, 0x4add0010 $BC1 + = 0x10$ add ebx, 0x40add0010 $BC1 + = 0x10$ add ebx, 0x10add0010 $BC1 + = 0x10$ add ebx, 0x40add0100 $BC1 + = 0x100$ add ebx, 0x400add0100 $BC1 + = 0x100$ add ebx, 0x400add0100 $BC1 + = 0x100$ add ebx, 0x400add0400 $BC1 + = 0x100$ add ebx, 0x400add1000 $BC1 + = 0x100$ add ebx, 0x4000sub0001 $BC1 - = 0x1$ sub ebx, 0x1shl $BC1 < (BC2 \& 0xFF)$ shl ebx, clshr $BC1 >> (BC2 \& 0xFF)$ shl ebx, clshr $BC1 \& BC2$ and ebx, ecxand $BC1 \& BC2$ and ebx, ecx	getDO	BC1 = DataOffset	mov ebx, DataOffset
pushpush BC1push ebxpoppop BC1pop ebxpushallpushadpushadpopallpopadpopadzer0BC1 = 0mov ebx, 0x0add0001BC1 + = 0x1add ebx, 0x1add0004BC1 + = 0x4add ebx, 0x4add0010BC1 + = 0x10add ebx, 0x40add0100BC1 + = 0x10add ebx, 0x40add0100BC1 + = 0x100add ebx, 0x40add0100BC1 + = 0x100add ebx, 0x400add0100BC1 + = 0x100add ebx, 0x400add0400BC1 + = 0x100add ebx, 0x400add1000BC1 + = 0x100add ebx, 0x400add4000BC1 + = 0x100add ebx, 0x400add1000BC1 + = 0x100add ebx, 0x400add4000BC1 + = 0x100add ebx, 0x400sub0001BC1 - = 0x1sub ebx, 0x1shlBC1 < < (BC2 & 0xFF)	getdata	BC1 = dword[BC1]	mov ebx, dword[ebx]
poppop BC1pop ebxpushallpushadpushadpopallpopadpopadzer0BC1 = 0mov ebx, 0x0add0001BC1 + = 0x1add ebx, 0x1add0004BC1 + = 0x4add ebx, 0x4add0010BC1 + = 0x40add ebx, 0x40add0100BC1 + = 0x40add ebx, 0x40add0100BC1 + = 0x400add ebx, 0x400add0400BC1 + = 0x100add ebx, 0x400add0400BC1 + = 0x400add ebx, 0x400add0400BC1 + = 0x1000add ebx, 0x400add1000BC1 + = 0x1000add ebx, 0x400add2000BC1 + = 0x1000add ebx, 0x4000sub0001BC1 - 0x1sub ebx, 0x1shlBC1 << (BC2 & 0xFF)	getEIP	BC1 = InstructionPointer	call gEIP; gEIP: pop ebx
pushallpushadpushadpopallpopadpopadzer0BC1 = 0mov ebx, 0x0add0001BC1 + = 0x1add ebx, 0x1add0004BC1 + = 0x4add ebx, 0x4add0010BC1 + = 0x10add ebx, 0x40add0040BC1 + = 0x40add ebx, 0x40add0100BC1 + = 0x400add ebx, 0x400add0100BC1 + = 0x400add ebx, 0x400add0400BC1 + = 0x400add ebx, 0x400add1000BC1 + = 0x400add ebx, 0x4000add2000BC1 + = 0x4000add ebx, 0x4000add1000BC1 + = 0x4000add ebx, 0x4000add2000BC1 + = 0x1000add ebx, 0x4000add1000BC1 - = 0x1sub ebx, 0x1subo001BC1 <- (BC2 & 0xFF)	push	push BC1	push ebx
popall         popad         popad           zer0         BC1 = 0         mov ebx, 0x0           add0001         BC1 + = 0x1         add ebx, 0x1           add0004         BC1 + = 0x4         add ebx, 0x4           add0010         BC1 + = 0x10         add ebx, 0x40           add0000         BC1 + = 0x10         add ebx, 0x40           add0000         BC1 + = 0x100         add ebx, 0x400           add0100         BC1 + = 0x100         add ebx, 0x400           add0400         BC1 + = 0x100         add ebx, 0x400           add0400         BC1 + = 0x100         add ebx, 0x400           add0400         BC1 + = 0x100         add ebx, 0x400           add000         BC1 + = 0x1000         add ebx, 0x400           add4000         BC1 + = 0x400         add ebx, 0x4000           sub0001         BC1 - = 0x1         sub ebx, 0x1           shl         BC1 <= 0x1         sub ebx, 0x1           shr         BC1 >> (BC2 & 0xFF)         shl ebx, cl           shr         BC1 ^ = BC2         xor ebx, ecx           and         BC1 & = BC2         and ebx, ecx           mul         (RegD: RegA) = RegA * BC1         mul ebx           div         (RegD, RegA) = RegA / BC1	pop	pop BC1	pop ebx
zer0         BC1 = 0         mov ebx, 0x0           add0001         BC1 + = 0x1         add ebx, 0x1           add0004         BC1 + = 0x4         add ebx, 0x4           add0010         BC1 + = 0x10         add ebx, 0x40           add0040         BC1 + = 0x10         add ebx, 0x40           add0100         BC1 + = 0x100         add ebx, 0x40           add0100         BC1 + = 0x100         add ebx, 0x100           add0400         BC1 + = 0x100         add ebx, 0x400           add1000         BC1 + = 0x100         add ebx, 0x400           add1000         BC1 + = 0x1000         add ebx, 0x400           add4000         BC1 + = 0x400         add ebx, 0x400           add4000         BC1 + = 0x400         add ebx, 0x4000           sub001         BC1 - = 0x1         sub ebx, 0x1           shl         BC1 < < (BC2 & 0xFF)         shl ebx, cl           shr         BC1 << BC2         xor ebx, ecx           and         BC1 & = BC2         and ebx, ecx           mul         (RegD: RegA) = RegA * BC1         mul ebx           div         (RegD, RegA) = RegA / BC1         div ebx           JnzUp         jz over; jmp esi; over:         jnz down; times 32: nop; down:           call <th>pushall</th> <th>pushad</th> <th>pushad</th>	pushall	pushad	pushad
add0001       BC1 + = 0x1       add ebx, 0x1         add0004       BC1 + = 0x4       add ebx, 0x4         add0010       BC1 + = 0x10       add ebx, 0x10         add0040       BC1 + = 0x10       add ebx, 0x40         add0100       BC1 + = 0x100       add ebx, 0x40         add0100       BC1 + = 0x100       add ebx, 0x400         add0400       BC1 + = 0x100       add ebx, 0x400         add0400       BC1 + = 0x400       add ebx, 0x400         add4000       BC1 + = 0x400       add ebx, 0x400         add4000       BC1 + = 0x4000       add ebx, 0x4000         sub0001       BC1 - = 0x1       sub ebx, 0x1         shl       BC1 <- (BC2 & 0xFF)	popall	popad	popad
add0004 $BC1 + = 0x4$ add ebx, 0x4         add0010 $BC1 + = 0x10$ add ebx, 0x10         add0040 $BC1 + = 0x40$ add ebx, 0x40         add0100 $BC1 + = 0x100$ add ebx, 0x100         add0400 $BC1 + = 0x100$ add ebx, 0x100         add1000 $BC1 + = 0x100$ add ebx, 0x400         add1000 $BC1 + = 0x100$ add ebx, 0x400         add4000 $BC1 + = 0x1000$ add ebx, 0x4000         add4000 $BC1 + = 0x4000$ add ebx, 0x4000         add4000 $BC1 + = 0x4000$ add ebx, 0x4000         sub0001 $BC1 - = 0x1$ sub ebx, 0x1         shl $BC1 < = 0x1$ sub ebx, 0x1         shr $BC1 >> (BC2 \& 0xFF)$ shl ebx, cl         shr $BC1 >> (BC2 \& 0xFF)$ shl ebx, cl         xor $BC1 & = BC2$ xor ebx, ecx         and $BC1 \& = BC2$ and ebx, ecx         mul       (RegD; RegA) = RegA * BC1       mul ebx         div       (RegD, RegA) = RegA / BC1       div ebx         JnzUp       jz over; jmp esi; over:         JnzDown       jnz down; times 32: nop; down:         call       stdcall	zer0	BC1 = 0	mov ebx, 0x0
add0010 $BC1 + = 0x10$ add ebx, $0x10$ add0040 $BC1 + = 0x40$ add ebx, $0x40$ add0100 $BC1 + = 0x100$ add ebx, $0x100$ add0400 $BC1 + = 0x400$ add ebx, $0x400$ add1000 $BC1 + = 0x400$ add ebx, $0x400$ add4000 $BC1 + = 0x400$ add ebx, $0x400$ add4000 $BC1 + = 0x4000$ add ebx, $0x4000$ add4000 $BC1 + = 0x4000$ add ebx, $0x4000$ sub0001 $BC1 - = 0x1$ sub ebx, $0x1$ shl $BC1 < (BC2 \& 0xFF)$ shl ebx, cl         shr $BC1 > (BC2 \& 0xFF)$ shr ebx, cl         xor $BC1 \wedge = BC2$ xor ebx, ecx         and $BC1 \& = BC2$ and ebx, ecx         mul       (RegD: RegA) = RegA * BC1       mul ebx         div       (RegD, RegA) = RegA / BC1       div ebx         JnzUp       jz over; jmp esi; over:         JnzDown       jnz down; times 32: nop; down:         call       stdcall ebx	add0001	BC1 + = 0x1	add ebx, 0x1
add0040 $BC1 + = 0x40$ add ebx, 0x40         add0100 $BC1 + = 0x100$ add ebx, 0x100         add0400 $BC1 + = 0x400$ add ebx, 0x400         add1000 $BC1 + = 0x400$ add ebx, 0x400         add4000 $BC1 + = 0x1000$ add ebx, 0x1000         add4000 $BC1 + = 0x4000$ add ebx, 0x4000         sub0001 $BC1 - = 0x1$ sub ebx, 0x1         shl $BC1 << (BC2 \& 0xFF)$ shl ebx, cl         shr $BC1 >> (BC2 \& 0xFF)$ shr ebx, cl         xor $BC1 \wedge = BC2$ xor ebx, ecx         and $BC1 \& = BC2$ and ebx, ecx         mul       (RegD: RegA) = RegA * BC1       mul ebx         div       (RegD, RegA) = RegA / BC1       div ebx         JnzUp       jz over; jmp esi; over:       jnz down; times 32: nop; down:         call       stdcall ebx	add0004	BC1 + = 0x4	add ebx, 0x4
add0100 $BC1 + = 0x100$ add ebx, $0x100$ add0400 $BC1 + = 0x400$ add ebx, $0x400$ add1000 $BC1 + = 0x1000$ add ebx, $0x1000$ add4000 $BC1 + = 0x4000$ add ebx, $0x4000$ sub0001 $BC1 - = 0x1$ sub ebx, $0x1$ shl $BC1 <= 0x1$ sub ebx, $0x1$ shr $BC1 >> (BC2 \& 0xFF)$ shl ebx, cl         shr $BC1 >> (BC2 \& 0xFF)$ shl ebx, cl         xor $BC1 \wedge = BC2$ xor ebx, ecx         and $BC1 \& = BC2$ and ebx, ecx         mul       (RegD: RegA) = RegA * BC1       mul ebx         div       (RegD, RegA) = RegA / BC1       div ebx         JnzUp       jz over; jmp esi; over:         JnzDown       jnz down; times 32: nop; down:         call       stdcall ebx	add0010	BC1 + = 0x10	add ebx, 0x10
add0400 $BC1 + = 0x400$ add ebx, $0x400$ add1000 $BC1 + = 0x1000$ add ebx, $0x1000$ add4000 $BC1 + = 0x4000$ add ebx, $0x4000$ sub0001 $BC1 - = 0x1$ sub ebx, $0x1$ shl $BC1 << (BC2 \& 0xFF)$ shl ebx, cl         shr $BC1 >> (BC2 \& 0xFF)$ shr ebx, cl         xor $BC1 \wedge = BC2$ xor ebx, ecx         and $BC1 \& = BC2$ and ebx, ecx         mul       (RegD: RegA) = RegA * BC1       mul ebx         div       (RegD, RegA) = RegA / BC1       div ebx         JnzUp       jz over; jmp esi; over:         JnzDown       jnz down; times 32: nop; down:	add0040	BC1 + = 0x40	add ebx, 0x40
add1000 $BC1 + = 0x1000$ add ebx, $0x1000$ add4000 $BC1 + = 0x4000$ add ebx, $0x4000$ sub0001 $BC1 - = 0x1$ sub ebx, $0x1$ shl $BC1 < < (BC2 & 0xFF)$ shl ebx, clshr $BC1 >> (BC2 & 0xFF)$ shr ebx, clxor $BC1 \wedge = BC2$ xor ebx, ecxand $BC1 \& = BC2$ and ebx, ecxmul(RegD: RegA) = RegA * BC1mul ebxdiv(RegD, RegA) = RegA / BC1div ebxJnzUpjz over; jmp esi; over:JnzDownjnz down; times 32: nop; down:callstdcall ebx	add0100	BC1 + = 0x100	add ebx, 0x100
add4000 $BC1 + = 0x4000$ add ebx, $0x4000$ sub0001 $BC1 - = 0x1$ sub ebx, $0x1$ shl $BC1 << (BC2 & 0xFF)$ shl ebx, clshr $BC1 >> (BC2 & 0xFF)$ shr ebx, clxor $BC1 \wedge = BC2$ xor ebx, ecxand $BC1 \& = BC2$ and ebx, ecxmul(RegD: RegA) = RegA * BC1mul ebxdiv(RegD, RegA) = RegA / BC1div ebxJnzUpjz over; jmp esi; over:JnzDownjnz down; times 32: nop; down:callstdcall ebx	add0400	BC1 + = 0x400	add ebx, 0x400
sub0001 $BC1 - = 0x1$ sub ebx, $0x1$ shl $BC1 << (BC2 & 0xFF)$ shl ebx, clshr $BC1 >> (BC2 & 0xFF)$ shr ebx, clxor $BC1 >> (BC2 & 0xFF)$ shr ebx, cland $BC1 \wedge = BC2$ xor ebx, ecxand $BC1 \& = BC2$ and ebx, ecxmul(RegD: RegA) = RegA * BC1mul ebxdiv(RegD, RegA) = RegA / BC1div ebxJnzUpjz over; jmp esi; over:JnzDownjnz down; times 32: nop; down:callstdcall ebx	add1000	BC1 + = 0x1000	add ebx, 0x1000
shl $BC1 << (BC2 \& 0xFF)$ shl ebx, clshr $BC1 >> (BC2 \& 0xFF)$ shr ebx, clxor $BC1 \land = BC2$ xor ebx, ecxand $BC1 \& = BC2$ and ebx, ecxmul(RegD: RegA) = RegA * BC1mul ebxdiv(RegD, RegA) = RegA / BC1div ebxJnzUpjz over; jmp esi; over:JnzDownjnz down; times 32: nop; down:callstdcall ebx	add4000	BC1 + = 0x4000	add ebx, 0x4000
shr $BC1 >> (BC2 \& 0xFF)$ shr ebx, clxor $BC1 \land = BC2$ xor ebx, ecxand $BC1 \& = BC2$ and ebx, ecxmul(RegD: RegA) = RegA * BC1mul ebxdiv(RegD, RegA) = RegA / BC1div ebxJnzUpjz over; jmp esi; over:JnzDownjnz down; times 32: nop; down:callstdcall ebx	sub0001	BC1 - = 0x1	sub ebx, 0x1
xor $BC1 \land = BC2$ xor ebx, ecxand $BC1 \& = BC2$ and ebx, ecxmul(RegD: RegA) = RegA * BC1mul ebxdiv(RegD, RegA) = RegA / BC1div ebxJnzUpjz over; jmp esi; over:JnzDownjnz down; times 32: nop; down:callstdcall ebx	shl	BC1 << (BC2 & 0xFF)	shl ebx, cl
andBC1 & = BC2and ebx, ecxmul(RegD: RegA) = RegA * BC1mul ebxdiv(RegD, RegA) = RegA / BC1div ebxJnzUpjz over; jmp esi; over:JnzDownjnz down; times 32: nop; down:callstdcall ebx	shr	BC1 >> (BC2 & 0xFF)	shr ebx, cl
mul(RegD: RegA) = RegA * BC1mul ebxdiv(RegD, RegA) = RegA / BC1div ebxJnzUpjz over; jmp esi; over:JnzDownjnz down; times 32: nop; down:callstdcall ebx	xor	$BC1 \land = BC2$	xor ebx, ecx
div(RegD, RegA) = RegA / BC1div ebxJnzUpjz over; jmp esi; over:JnzDownjnz down; times 32: nop; down:callstdcall ebx	and	BC1 & = BC2	and ebx, ecx
JnzUp     jz over; jmp esi; over:       JnzDown     jnz down; times 32: nop; down:       call     stdcall ebx	mul	(RegD: RegA) = RegA * BC1	mul ebx
JnzDown     jnz down; times 32: nop; down:       call     stdcall ebx	div	(RegD, RegA) = RegA / BC1	div ebx
call stdcall ebx	JnzUp		jz over; jmp esi; over:
	JnzDown		jnz down; times 32: nop; down:
CallAPILoadLibrary     stdcall dword[LoadLibrary]	call		stdcall ebx
-   -   -   -   -   -   -   -   -   -	CallAPILoadLibrary		stdcall dword[LoadLibrary]

#### 2.1.1 Mutable API calls

To use the APIs provided by the OS, an organism can use the call instruction:

```
DataOffset:
 [...]
    hGetCommandLineA dd OxO
                                ; should contain
 [...]
                                ; addresse of API
.code
start:
; [...]
    getDO
                                ; BC1=DataOffset
    addnumber (hGetCommandLineA-DataOffset)
                                ; macro to add number
                                ; BC1=dword[hGetCommandLineA]
    getdata
   _call
                                ; call BC1 :)
; [...]
.end start
```

One possibility is to save the API addresses directly within the organism body. However, this has two disadvantages: Firstly, the addresses may change for each new version of Windows. Secondly, the probability that a different and valid API address appears out of one single bitflip is very low (a rough estimate: a memory address of 32-bit gives  $2^{32}$  possible addresses - say there are 5000 valid API addresses. The probability to reach one of them is  $P = \frac{5000}{2^{32}} \approx 10^{-6}$ ).

A different method is to save a short hash of the desired API name in the organism. Then load the DLL, scan the export section for API names and create hashes for each API. If the hashes match, save the address of the API. This approach is independent of the OS version, and it's very mutable. It is possible to use hash as short as 12bit for each API. Let's say there are 1000 APIs in a DLL file. The probability to access on of these APIs within one single bitflip is given by  $P = \frac{1000}{2^{12}} \approx 0.25$ . In average, every 4th bitflip in the API hash leads to a different hash corresponding to a valid API.

#### 2.1.2 Example: ROL instruction

The x86 instruction ROL (*Rotate Left*) is not directly provided by the instruction set. However, it can be written using just instructions of the set.

Let's say, one would like to write rol RegA, c, where c is some integer - then the usual assembler instructions looks like this:

```
mov Reg2, RegA
sh1 Reg2, c
shr RegA, (32 - c)
xor RegA, Reg2
```

Translating this into the ArtEvol MetaLanguage is very easy:

zer0 addnumber save			BC1=c BC2=c
nopsA shl			BC1=Regà BC1=BC1 shl BC2 = Regà shl c
push		;	temporarly save at stack
	1 C C		; BC1=(32-c) BC2=(32-c)
nopsA shr			BC1=RegA BC1=BC1 shr BC2 = RegA shr (32-c)
save		;	BC2=RegA shr (32-c)
pop xor		;	restore value (Reg& shl c) BC1 xor BC2 = (Reg& shl c) XOR (Reg& shr (32-c))
nopdA			Reg&=(Reg& shl c) XOR (Reg& shr (32-c)) = Reg& rol c

The 2nd argument (c in this case) is saved in the BC2 register, then the first argument is loaded into the BC1 register and the operation is performed. As ROL requires three operations, the result of the first one is temporarily saved at the stack; the same procedure is performed again, and in the end, the results are combined.

The addnumber is a macro which returns the right combination of addNNNN instructions.

### 2.2 Translator

In order to convert the metalanguage instructions to native x86 instructions, a tiny translator is used:

```
.code
start:
     invoke VirtualAlloc, 0x0, (EndAmino-StAmino) *8, \lambda
            Ox1000, PAGE EXECUTE READWRITE
    mov
            [Place4Life], eax
            edx, 0x0
                                          ; EDX ... codon counter
    mov
    WriteMoreToMemory:
            mov
                    ebx, OxO
                                         ; EBX = 0
            mov
                    bl, byte[edx+StAmino] ; BL = current codon
                                         ; EBX*= 8
            shl
                    ebx, 3
                    esi, StartAlphabet ; ESI = alphabet offset
            mov
                                         ; ESI = offset for current
            add
                    esi, ebx
                                                 amino acid
                                          :
            mov
                    ebx, edx
                                         ; EBX = codon counter
                    ebx, 3
                                          ; EBX*=8 = size(amino acids)
            shl
                    edi, [Place4Life]
                                         ; EDI = memory address
            mov
                    edi, ebx
            add
                                          ; memory offset of current
                                          ; amino acid
                                          ; ECX=8
            mov
                    ecx, 8
                                          ; write 8 bytes from alphabet
                    movsb
            rep
                                          ; to memory
            inc
                    edx
                                          ; increase counter
    cmp
            edx, (EndAmino-StAmino)
            WriteMoreToMemory
                                         ; transformed all codons?
     jne
                                         ; let's start!!!
    call
            [Place4Life]
StartAlphabet:
    _pushall EQU 0 ; 0000 0000 - 0:
    CommandO: pushad
    times (8-$+CommandO): nop
     nopsà EQU 1
                   ; 0000 0001 - 1:
    Command1: mov ebx, eax
    times (8-$+Command1): nop
;[...]
StAmino:
    ; Code written in the ArtEvol metalanguage
EndAmino:
.end start
```

#### 2.3 Replication, Mutation and Selection

To achieve Evolution, a system requires Reproduction, Mutations and Selection.

In the **reproduction** stage, the organism creates a living copy of itself. In the artificial system of a computer, this can be done in the same way as computer viruses and computer worms do - interfere with special file formats or network protocols such that the copy will be executed in a different habitat (other computer, other file, ...). However, this requires alot of previous knowledge about the system, thus is not the simplest starting point for evolution. The most trivial way of reproduction is to copy the own file in the current directory and run it - which is actually the way how it is done in the experiments.

The interference between reproduction and **mutations** leads to non-identical replica of the organism. In the biological system, mutations happens due to disruptive effect such as cosmic X-rays. This leads to point mutations (exchange of one single codon) or more difficult chromosome abnormality. The natural mutation probability in a computer system (such as mistakes in the copy process) is neglectable, thus the organism has to carry its own mutation engines. This is explained in detail in the next chapter.

Natural selection is a process in which a certain trait becomes more or less common in the population, depending on its effect on the fitness of the organism. This process appears when the organism compete, struggle for limited values (such as energy), or are exposed to natural enemies. In the artificial system of a computer, anti virus programs could be responsible for natural selection (and by that unwillingly initiate a faster evolutionary process at all). A different selective pressure comes from attentive user, who would stop any suspicious behaviour.

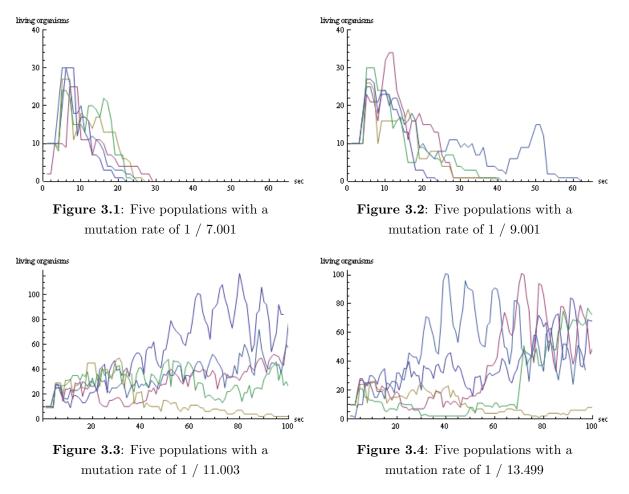
## 3 Mutations

#### 3.1 Point mutation - bit flip

Point mutations change single nucleobases in the DNA. These mutations can be categorized into silent (when the affected codon maps to the same amino acid due to the redundance in the alphabet), missense (when the affected codon maps to a different amino acid); or nonsense (when the affected codon maps to the STOP codon).

A native analogon to that concept would be the change of single bits in the organism - called *Bitflip*. These mutations have the same categories as their biological companion.

The mutation rate (mutations per base per generation) in biological organisms varies from  $10^{-4}$  for very small (some kilo bases) to  $10^{-8} - 10^{-9}$  for humans (some giga bases). Finding an adequate mutation rate for artificial organisms is not trivial, as too small values lead to mainly unmutated offspring, thus no evolution; whereas too big mutation rates lead to extinction of the population. To get the best, one could test organisms with different mutation rate, and take the critical value.

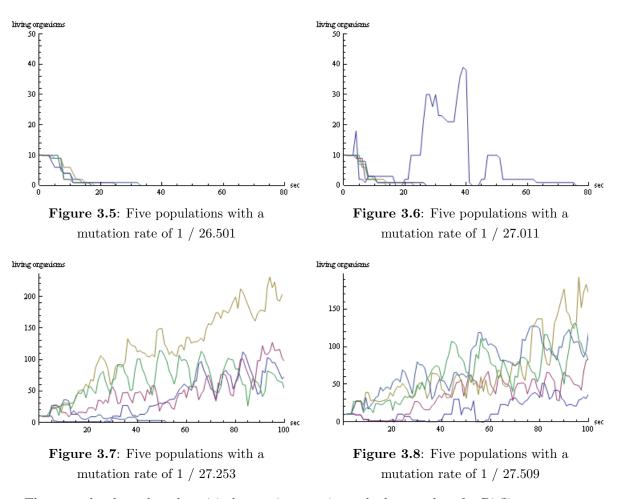


The test-organism - which can create three offspring - has a size of 20.480 bytes, the critical mutation rate is between  $\frac{1}{9.001}$  and  $\frac{1}{11.003}$ ; the probability that at least one bit is changed is between 84.5% and 89.7%.

## 3.2 Chromosomal inversion - byte eXCHanGe

A different kind of mutation happens when a segment of a chromosome is reversed. This happens when a segment breaks off and is rearranged in the wrong way.

A similar method is used in this project: two consecutive d-words (that means, 4 codons each) are exchanged. This mutation is not as dangerous as it may look like at first; the codon streams forming a functional part are very big, thus are not too sensitive on such local translocations.



These graphs show that the critical mutation rate is much sharper than for Bitflips this is what you would expect as Byte Exchange has much stronger effects.

#### 3.3 Deletion, insertion, translocation

Deletion is a mutation in which a part of the DNA is missing; insertion is the inverse process, where an additional sequence of DNA is included into the genom. Translocation is a combination of these mutations: A part of the DNA breaks off and is included at a different place.

A similar method can be realized in artificial organisms in the computer system within one simple algorithm. Three random values are calculated (the place of the mutation P, the size of the inserted NOP block  $S_i$  and the size of the translocated block  $S_b$ ). Then at P, a block with the size of  $S_b$  will be translocated  $S_i$  bytes, the new created block at P will be filled with NOPs.



Figure 3.9: Deletion process in the organism. Grey are NOPs, red and orange are functional parts. Red is not moved, orange is the translocated sequence. The second red part is smaller after deletion, as it has lost some of its code.



**Figure 3.10**: Insertion process in the organism. Grey are NOPs, red and orange are functional parts. Red is not moved, orange is the translocated sequence. The whole orange part is translocated, thus there is nothing deleted - just an insertion of NOPs.

### 3.4 Horizontal gene transfer

Horizontal gene transfer is a process in which an organism incorporates genetic material from another organism without being the offspring of that organism. In biological systems, this is a controlled (not by random mutations) method to receive beneficial functions such as antibiotic resistance. Photosynthesis is an important process which has been developed with horizontal gene transfer from different bacteria.

In the artificial system, an organism could try to interact with other organism and exchange valid code, and therefore perform a symbiotic *conjugation*. However, this would require a specific protocol for communication (such as F-plasmids in bacteria) - which is not developed so far.

Nevertheless the artificial organism could try to gain new information from other files, just by opening them and copy some parts of their code. In the case that the other file is written in the same language, the organism has the chance of getting new functions.

## 3.5 \*Polymorphism: neutral codon variation

In the artificial organisms, the alphabet has  $2^8 = 256$  entries which map to 45 or less instructions, thus there is a big redundancy - that means several codons map to the same amino acid.

The organism can scan thru its alphabet, detect equal amino acids, then scan its codon-stream and exchange the codons which point to the same amino acid.

There are a few advantages to use this technique in artificial organism: Firstly, codons which point to isolated amino acids (these who can not be transformed by a single bitflip to another amino acid of the same kind) can be de-isolated, thereby increase the robustness of the overall code. Secondly, such *macro mutations* are of high importance to bypass natural enemies (such as antivirus software), thus increase fitness. And thirdly, mutations in the polymorphism-engine itself or variations of the START or STOP codon could lead to unpredictable results.

## 4 Further improvements

### 4.1 Start- and Stop codons: Splicing

Natural genetic code contains alot of non-functional garbage, which can be old unused DNA or (malformed) duplicates of actual functional code. In human DNA, approximately 95% of the DNA is garbage. These non-coding parts are called *Introns*, the functional parts are *Exons*. Before translation of mRNA into proteins, the introns are cutted out in a process called *Splicing* - this is done by taking usage of two special codons - the *START* and the *STOP* codon. Each functional part starts with a START-codon and ends with a STOP-codon - the parts between a STOP codon and the next START codon (which is actually the intron) will be removed.

The advantage of such introns is that within the unused code new DNA sequences could be developed which are (in very rare cases) actually functional - and have different behaviour. Together with the additional possibility for altering its code, this would be a special gain for artificial organisms.

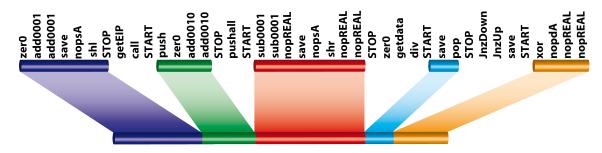


Figure 4.1: Splicing functionality in a artificial organism

Implementing a very small splicing algorithm into the translator can be achieved in the following way:

- All codons in the form of 1??1.???1 (32 codons) point to a NOP amino acid
- At the translation, whenever there is a STOP codon, AL=0x91 (1001.0001); whenever there is a START codon, AL=0x0
- Each codon will be OR'ed with AL
- In the end, each codon after a STOP mark will be redirected to a NOP amino acid until there is a START codon

```
invoke VirtualAlloc, 0x0, AlignedSize, \
        Ox1000, PAGE EXECUTE READWRITE
mov
        [Place4Life], eax
mov
        eax, 0x0
                              ; EAX ... Splicing seperator
        edx, 0x0
                              ; EDX ... codon counter
mov
WriteMoreToMemory:
        mov
                ebx, 0x0
                bl, byte[edx+StAmino] ; BL = current codon
        mov
                bl, StartCodon
                                      ; is it the Start Codon?
        cmp
        jne
                SplicingNoStart
                mov
                                      ; yes -> eax=0x0
                      eax, OxO
        SplicingNoStart:
                bl, StopCodon
                                      ; is it the Stop codon?
        cmp
                SplicingNoStop
        jne
                mov
                       eax, 0x91
                                      ; yes -> eax = 0x91
        SplicingNoStop:
                                              = 1001 0001
                                      ;
        or
                bl, al
                                      ; SPLICING!
                                      ; EBX*=8;
        shl
                ebx, 3
                                      ; ESI = alphabet offset
                esi, StartAlphabeth
        mov
                                      ; ESI = offset for current
                esi, ebx
        add
                                              amino acid
                                      2
              ebx, edx
      mov
                                      ; EBX = codon counter
      shl
              ebx, 3
                                      ; EBX*=8 = size(amino acids)
      mov
              edi, [Place4Life]
                                      ; EDI = memory address
              edi, ebx
                                      ; memory offset of current
      add
                                      ; amino acid
              ecx, 8
                                      ; ECX=8
      mov
                                      ; write 8 bytes from alphabet
      rep
              movsb
                                      ; to memory
      inc
              edx
                                      ; increase counter
        edx, (EndAmino-StAmino)
cmp
        WriteMoreToMemory
jne
                                      ; transformed all codons?
call
       [Place4Life]
                                      ; let's start!!!
```

#### 4.2 Optimization of the instruction set

The original instruction set had 43 different entries. Ofria, Adami and Collier found out, that a smaller instruction set leads to higher robustness under mutations, thus higher fitness[6].

In their experiments, the reason is that small instruction set requires bigger realisations of function, thus the risk of a lethal mutation is spread over a larger area. In our realisation, a second advantage appeares. A small instruction set leads to a more redundant alphabet, therefore allows more codons to point to the same amino acid. In the end, there is a bigger probability that a single BitFlip changes the codon such that it still points to the original amino acid.

Peter Ferrie was able to create an optimized instruction set with just 18 entries, by replacing an instruction with a combination of other instructions.[11] One simple example is  $zer0 \rightarrow save+xor$ .

For implementing these optimizations, one has to take care of changed Registers and Flags. This can be done by using Stack instruction (**pushall** and **popall**) or even implement a new *pseudo*register (in the .data section) with some special properties.

Unfortunately, these implementations lead to an excessive useage of *evolutionary* dangerous instructions, which are instructions that lead to the programs crash if they are replaced by other instructions.

I consider dangerous instructions as everything that interacts with the stack (push, pop, pushall, popall, CallAPILoadLibrary), that influences the code flow (JnzDown, JnzUp, call, saveJmpOff), and that interacts with the memory (saveWrtOff, writeByte, writeDWord, getdata). One can create two further categories: *semi-harmless*, which are all instructions that change the values of RegA, RegB, RegD and BC2, and *harmless* instructions just change the BC1 register.

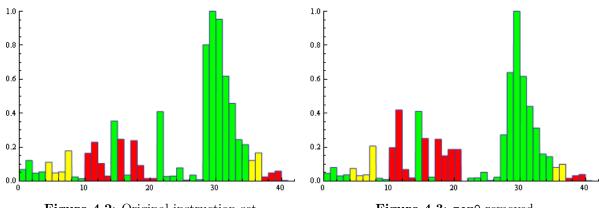
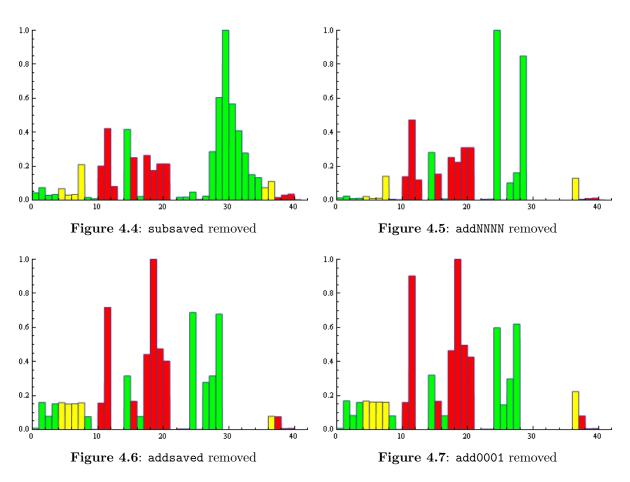


Figure 4.2: Original instruction set

Figure 4.3: zer0 removed



The x-axis shows the different instructions (with the same order as written in chapter 2), the y-axis gives the (normalized) appearence of the instruction in an organism. It is interesting to see how dangerous instruction density increases, especially after removing the addsaved instruction.

## 4.3 Optimization of the alphabet

To achieve the optimal robustness, evolution has lead to a special order of codon mapping. As an example, the amino acid *Proline* can be coded via CCU, CCC, CCA and CCG. That means, whenever a mutation changes the third nucleobase, still a codon remains that codes Prolin (in biological systems, mutations happens most often at the third nucleobase).

For artificial organisms, one has 256 slots for about 45 instructions, and furthermore different pairs of exchanged codons have different probability to cause mistakes (exchanging add0001 with add0004 may cause less problems than exchanging add0001 with pushall).

This is a nonlinear problem, and solutions by hand take long and are of low quality. However, one can reformulate the problem - with the help of a bit of physics: Let's imagine the codons as objects in a special space, such that each two codons with one bit difference are neighbors (the geometry of this space is an 8 dimensional cube with codons on the corners). For example, the codon 0000.0000 and 0010.0000 are neighbors. Each codon interacts with its neighbors, thus has an interaction energy (which depends on the types of the codons).

We can define V(A,B) as the interaction energy of codon A and B. One possible definition would be

$\mathbf{V}(\mathbf{A},\mathbf{B})=0$	if A = B				
V(A,B) = 0.5	if A and B are addNNNN or sub0001				
V(A,B) = 0.66	if A and B are harmless instructions				
V(A,B) = 0.75	if A and B are harmless or semi-harmless instructions				
V(A,B) = 1	else (if A or B is a dangerous instruction or a START or STOP codon)				
Now we can define the total energy of the system as:					
	256 8				

$$E_{\text{total}} = \sum_{i=1}^{250} \sum_{j=1}^{8} V(\text{codon}_i, \text{codon}_j)$$

The total energy of a maximum random system would be 2.048, the minumum total energy of a system with just one single instruction would be zero.

Finally, we can reformulate "find an optimal alphabet" into "find the minimal interaction energy of the system". There are several ways to find a minimum energy, one is the Metropolis-Algorithm; we use a slightly modified one.

First, we fill the 256 entries with instructions of random order, and calculate the total energy of the system. Then we exchange a few instructions randomly, and calculate the new total energy. If the new energy is smaller than the old one, we keep the new system, otherwise we continue with the old one. To find a (local) minimum, one can repeat that method.

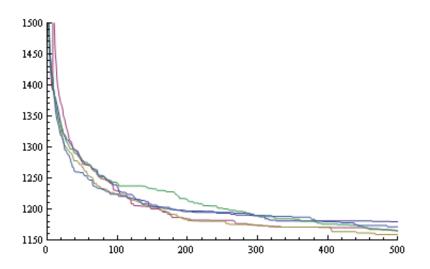


Figure 4.8: Energy minimization of five systems. x-axis is iteration number (in 1000s), y-axis is energy.

The figure shows that the alphabet reaches a good local minimum after a few 100.000 iterations. It also shows that systems with different starting order find local minimal of approximately the same energy.

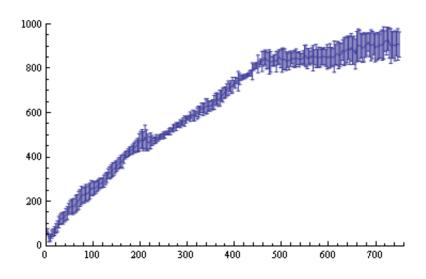
## 5 Experiments

An experiment measures the fitness of the organisms by letting them struggle for limited resources (such as CPU time and memory). To control the experiment, several *guard files* ran in the background, to close endless-loop files, multiple instances of the same file, unmutated files by a certain probability. A more detailed explanation can be found in [8].

### 5.1 Hamming distance

In the first experiment, we analyse the *long time* behaviour of a population, that can just perform BitFlips and Byte XCHG.

The Hamming distance (difference in the bit-code) with respect to the original ancestor is calculated every 3 minutes.

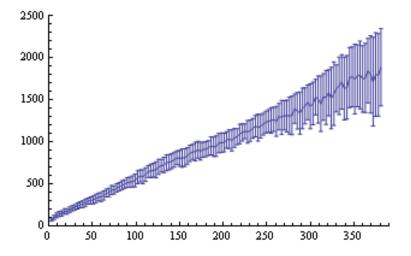


**Figure 5.1**: Evolution of Hamming distance over 12 hours. x-axis: Hamming distance, y-axis: time (in minutes)

It is very surprising and interesting, that after about 7.5 hours a mutation has occured with the effect that some organisms can largely bypass the no-cloning guard. A deeper analysis of that event would require reverse engineering of mutated code, which is a non-trivial task - and therefore hasn't been done yet.

However, this event is an indication that artificial organisms can bypass control instances very fast.

A second experiment has been performed, with an organism which contains about 90% introns (similar to natural organisms).



**Figure 5.2**: Evolution of Hamming distance for an organism with a high amount of introns over 6 hours. x-axis: Hamming distance, y-axis: time (in minutes)

As one would expect, the mutation rate is much higher and very constant, the standard derivation of the hamming distance spreads continuously.

## 5.2 Effect of alphabet "energy"

As explained in chapter 4.3, each alphabet has a specific "energy". To see whether the used definition of the interaction actually give a good result for robust alphabets, an evolutionary experiment has been performed.

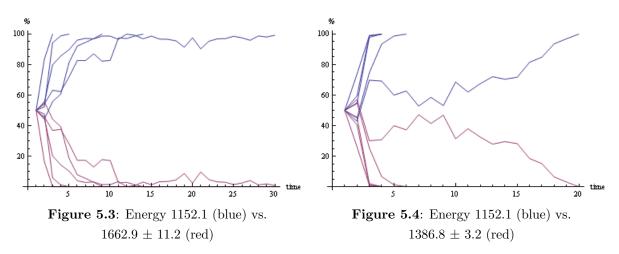


Figure 5.3 and 5.4 show that lower energy has a better fitness, thus the presented interaction definition is at least roughly a good approximation for an optimal alphabet.

#### 5.3 Effect of Start- and Stop-codons

One experiment has been performed with a number of 100.000 random codons within a STOP and a START codon - a big intron. Several organisms have converted a part of the intron into an exon (by introducing a START codon) without any negative effect - all converted functional parts were neutral mutations.

There were two very surprising results: The biggest converted part had 33 codons, and still worked without problem. A different converted exon even managed to perform an API call without crashing.

 33	<pre>functional START _add0001 _nopdD _getEIP _nopdD _pop _save _add0001 _div _nopdB _nopREAL _sub0001 _getEIP _add0001 _nopsA _add0001 _nopsA _add0001 _sh1 _save _add0001 _nopsA _add0001 _nopsA _add0001 _nopsA _add0001 _nopsA _add0001 _nopsA _add0001 _nopsA _add0001 _nopsA _add0001 _nopsA _add0001 _nopsA _add0001 _nopsA _add0001 _nopsA _add0001 _nopsA _add0001 _nopsA _add0001 _nopsB _nopsB _nopsB _nopdD _nopdB _nopdD _nopdD</pre>	codons:	19 functional codons (with an API call): 
	_STOP		

Such huge blocks of non-lethal codons and API calls could be very valuable for artificial organisms, which are chased by behaviour scanners and API call tracers.

## 6 Outlook

There are several features in the natural protein biosynthesis (or in microbiology in general) which could be used in this artificial concept:

Alternative Splicing: Before translating the codons into amino acids, the splicing process cuts out introns. In natural systems, there can be an alternative splicing process, which can create the final codon sequence in many alternative ways. For example, exons can be combined in different ways, some exons could be cutted out, introns can be coded, and other methods. This process is influenced by regulatory elements (such as proteins). An analogy to this process would increase the variability of the organisms drastically.

**Protein Folding:** After translation of codons into amino acids, a process called *folding* gives a specific 3D structure to the amino acid chain - this structure is mainly responsible for the proteins chemical properties. A similar further layer of translation may have advantages for the organisms too.

**Protocol for Horizontal Gene Transfer:** To exchange valuable information as antibiotic resistance, bacteria have developed processes called *conjugation*. To develope a process like that for exchanging useful information within the population would be a big advantage.

There are some further techniques which would increase the variability of the organisms, such as increasing the functional code size. The difficulties come from restrictions of the PE format, which requires more than one field to be mutated in the same manner. This is a very unlikely process - finding a solution to that problem would open many new possibilities for the organisms.

The experiments have shown a very promising behaviour of the different mutation techniques. Especially the START- and STOP-codon experiment, where organisms performed an additional API call, indicates that even macro mutations are realistic within this concept - and behaviour changing mutations are not always lethal.

The question of how antivirus programs can detect organisms using this technique is open. The organism's non-lethal configuration is infinite, and due to the fact that darwinian evolution is not predictable, algorithmic approaches are probably unusable and limited. Behaviour scanners are likely (due to effects that has been shown with Splicing) not practicable, too. Statistical approaches may work for a low number of generations quite good, but will most likely fail for big difference to the ancestor (at high generations or when several macro mutations happened), as well.[12].

... as a conclusion, one can see:

The artificial organisms took the redpill - and enjoy their new freedom now...

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